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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

KOMURA et al.

Art Unit: 2816

Application No.: 09/921,561

Examiner: Minh T. Nguyen

Filed: August 6, 2001

Attorney Dkt. No.: 024016-00012

For: DELAY CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
CONTAINING A DELAY CIRCUIT AND DELAY METHOD

**AMENDMENT UNDER 37CFR §1.111**

Commissioner for Patents  
Washington, D.C. 20231

Date: May 15, 2002

Sir:

This paper is filed in response to the outstanding Office Action dated January 15, 2002, the time period for response having been extended until May 15, 2002 by the enclosed petition for extension of time. In the prosecution of the application identified above, please consider the following amendments.

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**IN THE CLAIMS:**

Please amend the claims as follows:

1. (Amended)

A delay circuit comprising:

a delay section having two or more predetermined delay stages, each predetermined delay stage adds a predetermined delay time to an input signal; and  
selecting switch sections for establishing a delay path for the input signal  
by selecting one of the selecting switch sections, wherein